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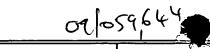
Micron Technology, Inc.

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PRIORITY FILING DATE 09/17/96 U.S. PATENT DOCUMENTS Class Subclass Filing DateU *Examiner Document Date Name If Appropriate Initial Number 4,971,655 11/20/90 Stefano et al. 156 659.1 12/26/89 4,981,810 1/1/91 Fazan et al. 437 44 2/16/90 AB 657 Blalock et al. AC 5,286,344 2/15/94 156 6/15/92 3/1/94 437 7/26/93 ΑD 5,290,720 Chen 41 257 ΑE 5,306,951 4/26/94 Lee et al. 755 5/14/92 5,334,556 8/2/94 437 248 3/23/93 ΑG 5,420,800 5/30/95 Fukui 364 491 9/2/94 437 187 12/17/93 ΑH 5.439.846 8/8/95 Nguyen et al. ΑI 4,786,609 11/1988 Chen 438 303 5,126,283 6/1992 Pintchovski 438 396 5,371,026 12/1994 438 275 FOREIGN PATENT DOCUMENTS Date Class Subclass Translation Document Country Number ΑL AM AN ΑO ΑP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) S. Wolf, et al., Silicon Processing For The VLSI Era, Lattice Press (1986), pp. 208 and 212, (month not available) AR AS ΑT **EXAMINER** DATE CONSIDERED

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